



The Bombay Salesian Society's

Don Bosco Institute of Technology, Kurla

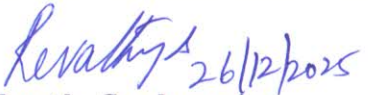


(An Autonomous Institute affiliated to University of Mumbai)

Department of Electronics and Telecommunication Engineering

Timetable (AY 2025-26 ODD Semester)

Semester – III

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Monday	05 January 2026	02:30 PM - 04:30 PM	25ET3PCC01	Mathematics for Signal Processing
2	Tuesday	06 January 2026	02:30 PM - 04:30 PM	25ET3PCC02	Linear Integrated Circuits
3	Wednesday	07 January 2026	02:30 PM - 04:30 PM	25ET3PCC03	Digital System Design using Verilog
4	Thursday	08 January 2026	02:30 PM - 04:30 PM	25ET3PCC04	Analog and Digital Communication
 Dr. Revathy Sundararajan (Controller of Examinations)				 Dr. Sudhakar Mande (Principal DBIT)	



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Department of Electronics and Telecommunication Engineering

Timetable (AY 2025-26 ODD Semester)

Semester – V

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Friday	02 January 2026	02:30 PM - 05:30 PM	ECC501	Digital Communications
2	Monday	05 January 2026	02:30 PM - 05:30 PM	ECC502	Discrete Time Signal Processing
3	Tuesday	06 January 2026	02:30 PM - 05:30 PM	ECC503	Digital VLSI
4	Wednesday	07 January 2026	02:30 PM - 05:30 PM	ECC504	Random Signal Analysis
5	Thursday	08 January 2026	02:30 PM - 05:30 PM	ECCDLO5012	DLOC – I: 1) Data Structure and Algorithm
				ECCDLO5015	2) Sensor Technology
6	Friday	09 January 2026	02:30 PM - 05:30 PM	HDSC501	Honours and Minors: 1) Mathematics for Data Science
				HEVC501	2) Vehicular Systems & Dynamics
				HAIMLC501	3) Mathematics for AI & ML
				HIoTC501	4) IoT Sensor Technologies
				HCSC501	5) Ethical Hacking
Revathy 26/12/2025 Dr. Revathy Sundararajan (Controller of Examnations)				CM Dr. Sudhakar Mande (Principal DBIT)	



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Department of Electronics and Telecommunication Engineering

Timetable (AY 2025-26 ODD Semester)

Semester – VII

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Friday	02 January 2026	02:30 PM - 05:30 PM	ECC701	Microwave Engineering
2	Monday	05 January 2026	02:30 PM - 05:30 PM	ECC702	Mobile Communication System
3	Tuesday	06 January 2026	02:30 PM - 05:30 PM	ECCDLO7014	Big Data Analytics
4	Wednesday	07 January 2026	02:30 PM - 05:30 PM	ECCDLO7023	Internet Communication Engineering
5	Thursday	08 January 2026	02:30 PM - 05:30 PM	ILO7013	ILOC - I: 1) Management Information System
				ILO7016	2) Cyber Security and Laws
				ILO7018	3) Energy Audit and Management
				ILO7019	4) Development Engineering
6	Friday	09 January 2026	02:30 PM - 05:30 PM	HAIMLC701	Honors & Minors: 1) AI&ML in Healthcare
				HCSC701	2) Security Information Management
				HDSC701	3) Data Science for Health & Social Care
				HloTC701	4) Dynamic Paradigm in IoT
				HEVC701	5) Automotive Controllers & Auxiliary Systems
<div>Revathy 26/12/2025 Dr. Revathy Sundararajan (Controller of Examnations)</div>			<div> Don Bosco Institute of Technology ENGG. COLLEGE</div>	<div>SM Dr. Sudhakar Mande (Principal DBIT)</div>	

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Department of Computer Engineering

Timetable (AY 2025-26 ODD Semester)

Semester – III

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Monday	05 January 2026	02:30 PM – 04:30 PM	25CE3PCC01	Discrete Structure and Graph Theory
2	Tuesday	06 January 2026	02:30 PM – 04:30 PM	25CE3PCC02	Analysis of Algorithm
3	Wednesday	07 January 2026	02:30 PM – 04:30 PM	25CE3PCC03	Computer Organization and Architecture
4	Thursday	08 January 2026	02:30 PM – 04:30 PM	25CE3PCC04	Database Management System
 Dr. Revathy Sundararajan (Controller of Examnations)					 Dr. Sudhakar Mande (Principal DBIT)



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Department of Computer Engineering

Timetable (AY 2025-26 ODD Semester)

Semester – V

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Friday	02 January 2026	02:30 PM - 05:30 PM	CSC501	Theoretical Computer Science
2	Monday	05 January 2026	02:30 PM - 05:30 PM	CSC502	Software Engineering
3	Tuesday	06 January 2026	02:30 PM - 05:30 PM	CSC503	Computer Network
4	Wednesday	07 January 2026	02:30 PM - 05:30 PM	CSC504	Data Warehousing & Mining
5	Thursday	08 January 2026	02:30 PM - 05:30 PM	CSDLO5011	DLOC – I: 1) Probabilistic Graphical Models
				CSDLO5012	2) Internet Programming
				CSDLO5013	3) Advance Database Management System
6	Friday	09 January 2026	02:30 PM - 05:30 PM	HDSC501	Honours and Minors: 1) Mathematics for Data Science
				HEVC501	2) Vehicular Systems & Dynamics
				HAIMLC501	3) Mathematics for AI & ML
				HIoTC501	4) IoT Sensor Technologies
				HCSC501	5) Ethical Hacking

Revathy 26/12/2025
Dr. Revathy Sundararajan
(Controller of Examnations)



Dr. Sudhakar Mande
Dr. Sudhakar Mande
(Principal DBIT)

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Semester – VII

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Friday	02 January 2026	02:30 PM - 05:30 PM	CSC701	Machine Learning
2	Monday	05 January 2026	02:30 PM - 05:30 PM	CSC702	Big Data Analytics
3	Tuesday	06 January 2026	02:30 PM - 05:30 PM	CSDC7013	Natural Language Processing
4	Wednesday	07 January 2026	02:30 PM - 05:30 PM	CSDC7022	Block Chain
5	Thursday	08 January 2026	02:30 PM - 05:30 PM	ILO7013	ILOC - I: 1) Management Information System
				ILO7016	2) Cyber Security and Laws
				ILO7018	3) Energy Audit and Management
				ILO7019	4) Development Engineering
6	Friday	09 January 2026	02:30 PM - 05:30 PM	HAIMLC701	Honors & Minors: 1) AI&ML in Healthcare
				HCSC701	2) Security Information Management
				HDSC701	3) Data Science for Health & Social Care
				HIoTC701	4) Dynamic Paradigm in IoT
				HEVC701	5) Automotive Controllers & Auxiliary Systems

Revathy S 26/12/2025
Dr. Revathy Sundararajan
(Controller of Examinations)



LM
Dr. Sudhakar Mande
(Principal DBIT)



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Department of Information Technology

Timetable (AY 2025-26 ODD Semester)

Semester – III

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Monday	05 January 2026	02:30 PM - 04:30 PM	25IT3PCC01	Computational Logic & Structures
2	Tuesday	06 January 2026	02:30 PM - 04:30 PM	25IT3PCC02	Advanced Data Structures & Algorithms
3	Wednesday	07 January 2026	02:30 PM - 04:30 PM	25IT3PCC03	Database Management System
4	Thursday	08 January 2026	02:30 PM - 04:30 PM	25IT3PCC04	Digital Logic & Computer Organization and Architecture
 Dr. Revathy Sundararajan (Controller of Examinations)				 Dr. Sudhakar Mande (Principal DBIT)	



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Department of Information Technology

Timetable (AY 2025-26 ODD Semester)

Semester – V

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Friday	02 January 2026	02:30 PM - 05:30 PM	ITC501	Internet Programming
2	Monday	05 January 2026	02:30 PM - 05:30 PM	ITC502	Computer Network Security
3	Tuesday	06 January 2026	02:30 PM - 05:30 PM	ITC503	Entrepreneurship and E-business
4	Wednesday	07 January 2026	02:30 PM - 05:30 PM	ITC504	Software Engineering
5	Thursday	08 January 2026	02:30 PM - 05:30 PM	ITDO5014	DLOC – I: Advanced Data Strutcure and Analysis
6	Friday	09 January 2026	02:30 PM - 05:30 PM	HDSC501	Honours and Minors: 1) Mathematics for Data Science
				HEVC501	2) Vehicular Systems & Dynamics
				HAIMLC501	3) Mathematics for AI & ML
				HIoTC501	4) IoT Sensor Technologies
				HCSC501	5) Ethical Hacking
<div>Revathy 26/12/2025 Dr. Revathy Sundararajan (Controller of Examinations)</div>			<div> GOMEAY SALESIAN SOCIETY ENGINEERING COLLEGE</div>	<div> Dr. Sudhakar Mande (Principal DBIT)</div>	



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Don Bosco Institute of Technology, Kurla
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Department of Information Technology

Timetable (AY 2025-26 ODD Semester)

Semester – VII

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Friday	02 January 2026	02:30 PM - 05:30 PM	ITC701	AI and DS - II
2	Monday	05 January 2026	02:30 PM - 05:30 PM	ITC702	Internet of Everything's
3	Tuesday	06 January 2026	02:30 PM - 05:30 PM	ITDO7013	Infrastructure Security
4	Wednesday	07 January 2026	02:30 PM - 05:30 PM	ITDO7024	Information Retrieval System
5	Thursday	08 January 2026	02:30 PM - 05:30 PM	ILO7013	ILOC - I: 1) Management Information System
				ILO7016	2) Cyber Security and Laws
				ILO7018	3) Energy Audit and Management
				ILO7019	4) Development Engineering
6	Friday	09 January 2026	02:30 PM - 05:30 PM	HAIMLC701	Honors & Minors: 1) AI&ML in Healthcare
				HCSC701	2) Security Information Management
				HDSC701	3) Data Science for Health & Social Care
				HloTC701	4) Dynamic Paradigm in IoT
				HEVC701	5) Automotive Controllers & Auxiliary Systems
<div>Revathy 26/12/2025 Dr. Revathy Sundararajan (Controller of Examinations)</div>			<div></div>	<div> Dr. Sudhakar Mande (Principal DBIT)</div>	



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Department of Mechanical Engineering

Timetable (AY 2025-26 ODD Semester)

Semester – III

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Monday	05 January 2026	02:30 PM - 04:30 PM	25ME3PCC01	Mathematics for Mechanical Engineering Applications
2	Tuesday	06 January 2026	02:30 PM - 04:30 PM	25ME3PCC02	Strength of Materials
3	Wednesday	07 January 2026	02:30 PM - 04:30 PM	25ME3PCC03	Manufacturing Processes
4	Thursday	08 January 2026	02:30 PM - 04:30 PM	25ME3PCC04	Thermodynamics
<i>Revathy</i> 26/12/2025 Dr. Revathy Sundararajan (Controller of Examinations)					<i>Dr. Sudhakar Mande</i> Dr. Sudhakar Mande (Principal DBIT)



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Department of Mechanical Engineering

Timetable (AY 2025-26 ODD Semester)

Semester – V

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Friday	02 January 2026	02:30 PM - 05:30 PM	MEC501	Mechanical Measurements and Controls
2	Monday	05 January 2026	02:30 PM - 05:30 PM	MEC502	Thermal Engineering
3	Tuesday	06 January 2026	02:30 PM - 05:30 PM	MEC503	Dynamics of Machinery
4	Wednesday	07 January 2026	02:30 PM - 05:30 PM	MEC504	Finite Element Analysis
5	Thursday	08 January 2026	02:30 PM - 05:30 PM	MEDLO5011	DLOC - I: 1) Optimization Techniques
				MEDLO5013	2) Computational Methods
6	Friday	09 January 2026	02:30 PM - 05:30 PM	HDSC501	Honours and Minors: 1) Mathematics for Data Science
				HEVC501	2) Vehicular Systems & Dynamics
				HAIMLC501	3) Mathematics for AI & ML
				HIoTC501	4) IoT Sensor Technologies
				HCSC501	5) Ethical Hacking
Dr. Revathy Sundararajan (Controller of Examinations)				Dr. Sudhakar Mande (Principal DBIT)	



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Department of Mechanical Engineering

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Semester – VII

Time Table for Supplementary Examinations – January 2026

Sr. No.	Day	Date	Timing	Paper Code	Paper
1	Friday	02 January 2026	02:30 PM - 05:30 PM	MEC701	Design of Mechanical System
2	Monday	05 January 2026	02:30 PM - 05:30 PM	MEC702	Logistics and Supply Chain Management
3	Tuesday	06 January 2026	02:30 PM - 05:30 PM	MEDLO7032	Renewable Energy Systems
				MEDLO7033	Vehicle Systems
4	Wednesday	07 January 2026	02:30 PM - 05:30 PM	MEDLO7042	Vibration Controls
5	Thursday	08 January 2026	02:30 PM - 05:30 PM	ILO7013	ILOC - I: 1) Management Information System
				ILO7016	2) Cyber Security and Laws
				ILO7018	3) Energy Audit and Management
				ILO7019	4) Development Engineering
6	Friday	09 January 2026	10:30 AM - 01:30 PM	HAIMLC701	Honors & Minors: 1) AI&ML in Healthcare
				HCSC701	2) Security Information Management
				HDSC701	3) Data Science for Health & Social Care
				HIoTC701	4) Dynamic Paradigm in IoT
				HEVC701	5) Automotive Controllers & Auxiliary Systems
Dr. Revathy Sundararajan (Controller of Examinations)				Dr. Sudhakar Mande (Principal DBIT)	